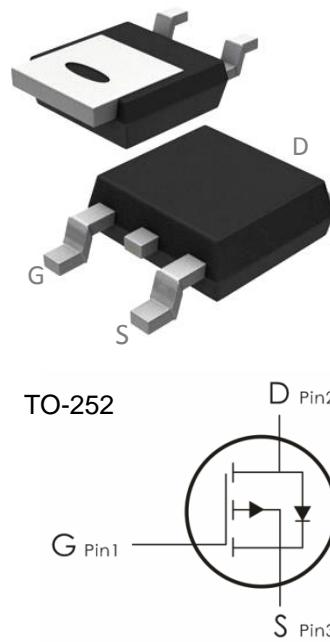


P-Channel 60 V (D-S) MOSFET

Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.



Features:

- 1) $V_{DS}=-60V, I_D=-30A, R_{DS(on)}<35m\Omega @V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.

Absolute Maximum Ratings: ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	-30	A
	Continuous Drain Current- $T_C=100^\circ C$	-19	
	Pulsed Drain Current ¹	---	
E_{AS}	Single Pulse Avalanche Energy	225	mJ
P_D	Power Dissipation	50	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case	3	$^\circ C/W$
R_{eJA}	Thermal Resistance,Junction to Ambient	42	

Electrical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\ \mu\text{A}$	-60	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-60\text{V}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\ \mu\text{A}$	-1	-1.8	-2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance ^②	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-15\text{A}$	---	26	35	$\text{m}\ \Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-10\text{A}$	---	32	40	
G_{FS}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-15\text{A}$	---	---	---	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	2535	---	pF
C_{oss}	Output Capacitance		---	130	---	
C_{rss}	Reverse Transfer Capacitance		---	75	---	
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-30\text{V}, I_{\text{D}}=-10\text{A}, R_{\text{GEN}}=6.8\ \Omega, V_{\text{GS}}=-10\text{V}$	---	14	---	ns
t_r	Rise Time		---	18	---	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		---	42	---	ns
t_f	Fall Time		---	15	---	ns
Q_g	Total Gate Charge	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-10\text{A}$	---	46	---	nC
Q_{gs}	Gate-Source Charge		---	11	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	10	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-15\text{A}, T_j=25^\circ\text{C}$	---	-0.88	-1.2	V

t_{rr}	Reverse Recovery Time	$I_{sd}=-20A, V_{GS}=0V$	---	28	---	ns
Q_{rr}	Reverse Recovery Charge		$.dI/dt=-500A/\mu s$	---	165	---

Notes:

1. Repetitive rating; pulse width limited by max. junction temperature.
2. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$.
3. Limited by T_{Jmax} , starting $T_J = 25 \mu A C$, $L = 0.5mH$, $R_G = 25 \Omega$, $I_{AS} = -32A$, $V_{GS} = -10V$. Part not recommended for use above this value

Typical Characteristics: ($T_C=25^\circ C$ unless otherwise noted)

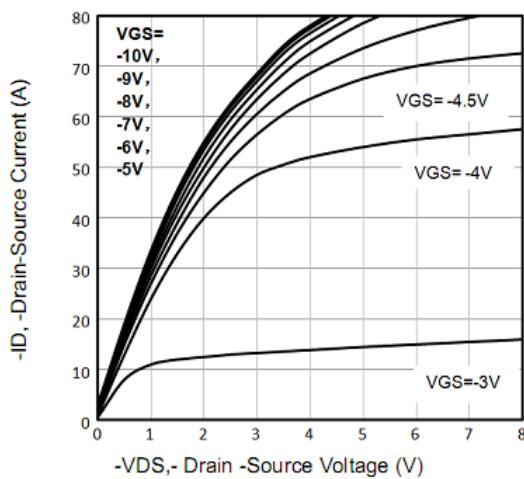


Fig1. Typical Output Characteristics

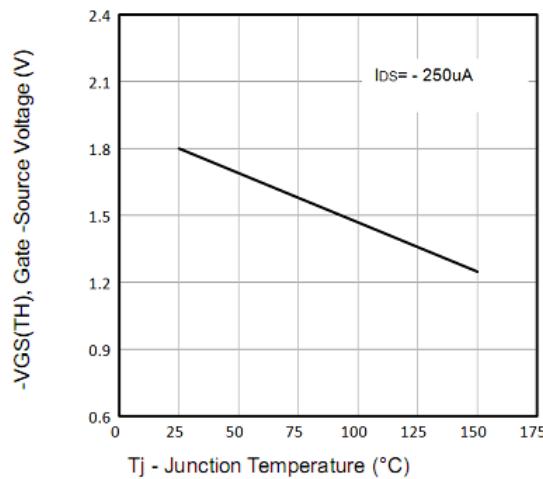


Fig2. $-VGS(TH)$ Gate-Source Voltage Vs. T_J

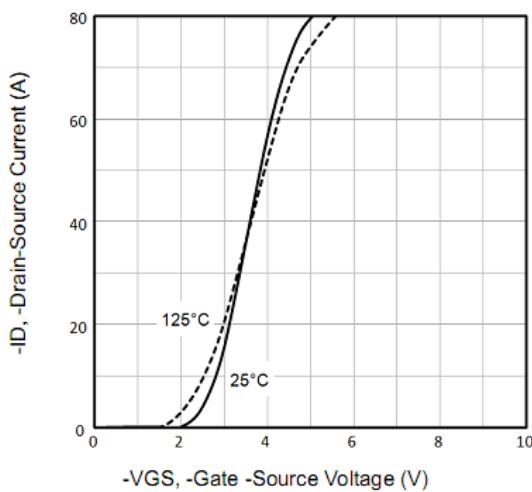


Fig3. Typical Transfer Characteristics

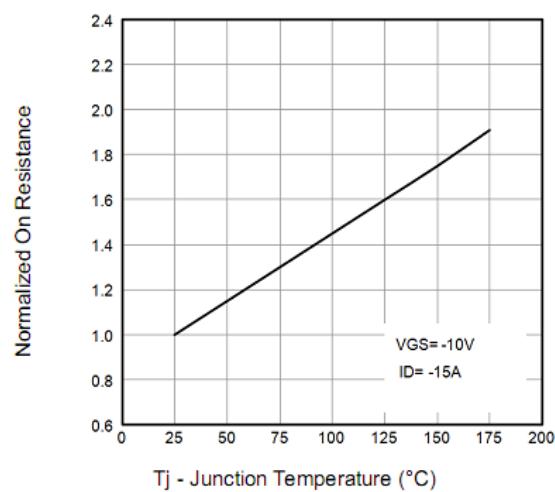


Fig4. Normalized On-Resistance Vs. T_J

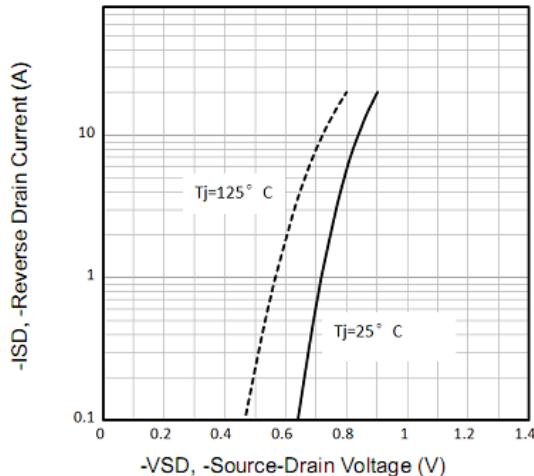


Fig 5. Typical Source-Drain Diode Forward Voltage

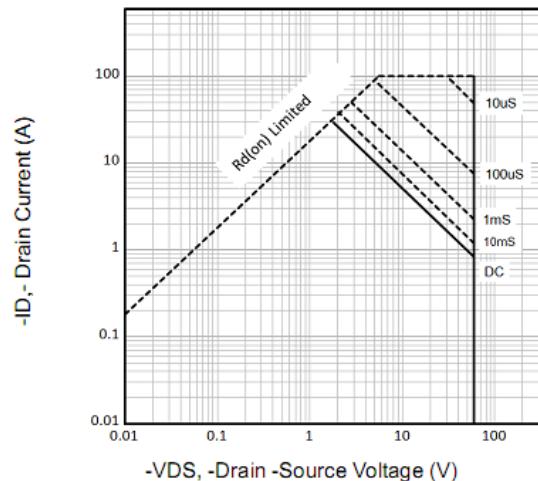


Fig 6. Maximum Safe Operating Area

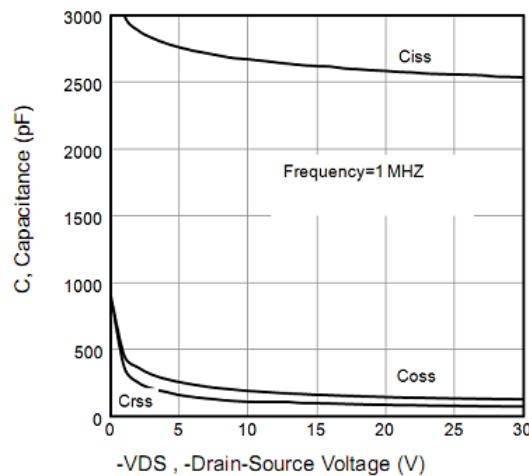


Fig 7. Typical Capacitance Vs. Drain-Source Voltage

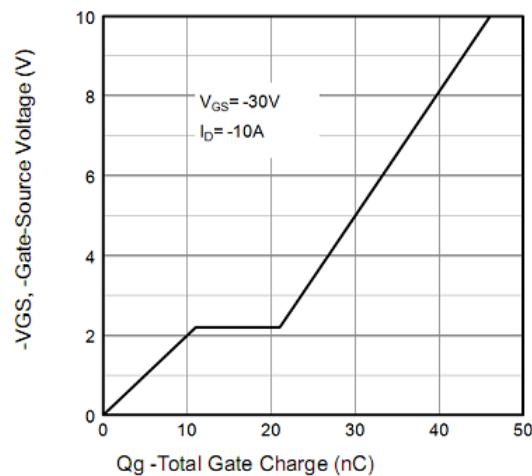


Fig 8. Typical Gate Charge Vs. Gate-Source Voltage

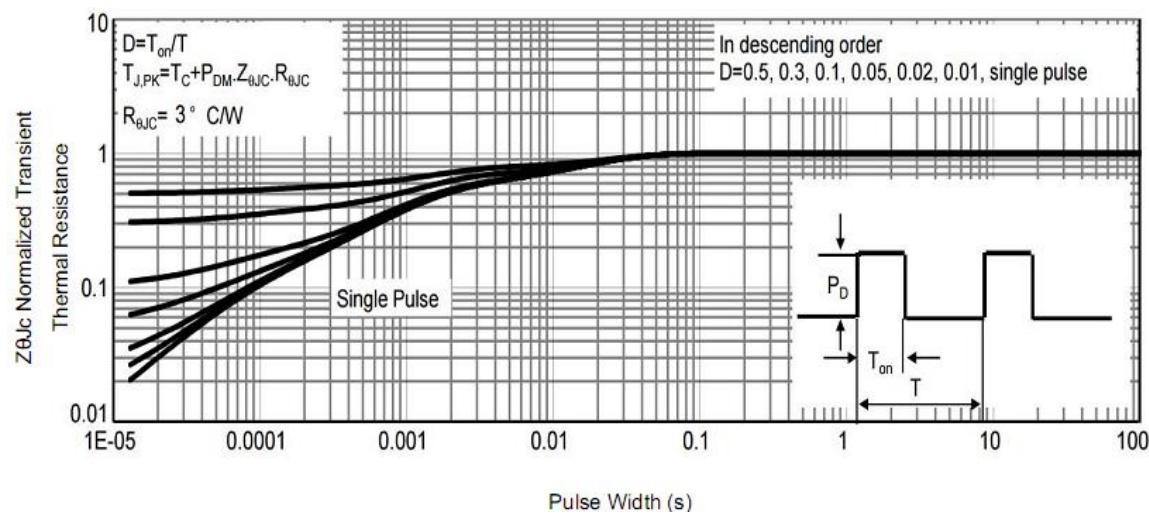


Fig 9. Normalized Maximum Transient Thermal Impedance

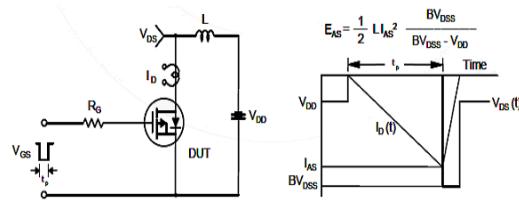


Fig 10. Unclamped Inductive Test Circuit and Waveforms

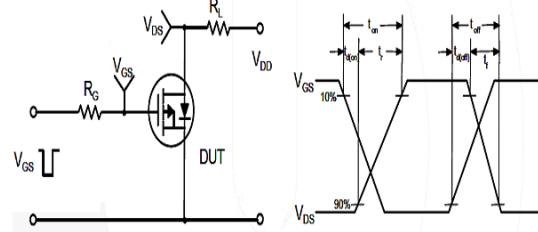


Fig 11. Switching Time Test Circuit and waveforms

外形尺寸图 / Package Dimensions

